

<b>Notice of Allowability</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	10/026,688	FUJIMOTO ET AL.	
	<b>Examiner</b>	<b>Art Unit</b>	
	VIJAY SHANKAR	2629	

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--**

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 1-18-2007.
2. ☒ The allowed claim(s) is/are 1-23,39, Renumbered as 1-24.
3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☒ All    b) ☐ Some\*    c) ☐ None    of the:
    1. ☒ Certified copies of the priority documents have been received.
    2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

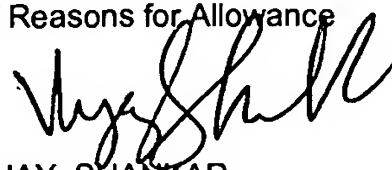
4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
  - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
    - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
  - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO/SB/08),  
Paper No./Mail Date \_\_\_\_\_.
4. ☐ Examiner's Comment Regarding Requirement for Deposit  
of Biological Material

5. ☐ Notice of Informal Patent Application
6. ☐ Interview Summary (PTO-413),  
Paper No./Mail Date \_\_\_\_\_.
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other \_\_\_\_\_.

  
 VIJAY SHANKAR  
 Primary Examiner  
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***Allowable Subject Matter***

1. Claims 1-23 and 39 are allowed.
2. The following is an examiner's statement of reasons for allowance: Applicant's argument filed on 01-18-2007 is convincing. As argued by the applicant on pages 2-7, the uniquely distinct features as claimed in Claim 1, supplying a source driver circuit with the branched plural-systems image data in synchronization with at least one clock signal having a clock frequency that is less than half of the original data rate; and allowing the source driver circuit to further branch the branched plural-systems image data into gray scale voltage signals is not shown or suggested by the prior arts Kuboki et al.
3. Also, As argued by the applicant on pages 2-7, the uniquely distinct features as claimed in Claim 9, a plurality of source driver circuits for incorporating the image data in synchronization with the at least one clock signal and converting the image data into gray scale voltage signals, wherein the timing controller comprises a branching unit for branching original image data having an original data rate into branched plural-systems image data comprising plural systems having a converted data rate that is equal to either the original data rate or a half of the original data rate is not shown or suggested by the prior arts Kuboki et al.

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4. The following is an examiner's statement of reasons for allowance: The prior arts Kuboki et al, Setogawa, Igarashi, fails to recite and disclose a method of driving a liquid crystal display device comprising: branching original image data having an original data rate into branched plural-systems image data comprising plural systems having a converted data rate which is equal to either the original data rate or a half of the original data rate ; supplying a source driver circuit with the branched plural-systems image data in synchronization with at least one clock signal having a clock frequency that is less than half of the original data rate; and allowing the source driver circuit to further branch the branched plural-systems image data into gray scale voltage signals as claimed in Claim 1.

5. Also, the prior arts Kuboki et al, Setogawa, Igarashi, fails to recite and disclose a circuitry for driving a liquid crystal display device comprising: a timing controller for generating image data and at least a clock signal; a plurality of data bus lines for transmitting the image data and at least a clock signal; and a plurality of source driver circuits for incorporating the image data in synchronization with the at least one clock signal and converting the image data into gray scale voltage signals, wherein the timing controller comprises a branching unit for branching original image data having an original data rate into branched plural-systems image data comprising plural systems having a converted data rate that is equal to either the original data rate or a half of the original data rate as claimed in Claim 9.

The closest prior art, Kuboki et al, Setogawa, Igarashi, either singularly or in combination, fail to anticipate or render the above bold and underlined limitations obvious.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to VIJAY SHANKAR whose telephone number is (571) 272-7682. The examiner can normally be reached on M-F 7:00 am - 4:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, BIPIN SHALWALA can be reached on (571) 272-7681. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.



VIJAY SHANKAR  
Primary Examiner  
Art Unit 2629

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